

BRUCE T. WIEDER

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BIOGRAPHY:

Bruce Wieder is a Member in the Media and Information Technologies practice group. He specializes in patent litigation at both the trial and appellate levels, as well as patent licensing and settlement agreements. He has extensive experience in a wide range of patent matters including the enforcement of our clients' patent rights and defending our clients against accusations of infringement. Mr. Wieder regularly counsels clients regarding patents and related intellectual property matters. He has lectured on and is experienced in analyzing patent infringement, validity and enforceability issues. He is also experienced in representing clients before the U.S. Court of Appeals for the Federal Circuit, federal district courts and the Board of Patent Appeals and Interferences at the U.S. Patent and Trademark Office. His experience also includes the protection of communications and other high technology systems as well as computer software. His primary technical experience is in VLSI circuit design.

Prior to joining Dow Lohnes, Mr. Wieder was a partner at Burns Doane Swecker & Mathis in the Alexandria, Virginia office. Mr. Wieder was also a law clerk to the Honorable S. Jay Plager, Circuit Judge, United States Court of Appeals for the Federal Circuit. He was also an engineer at Motorola, Inc., Microprocessor Design Group.

Mr. Wieder has been an Adjunct Professor at Georgetown University Law Center since 1997. He is also a Past President of the Giles S. Rich American Inn of Court, the oldest intellectual property focused American Inn of Court. He is registered to practice before the U.S. Patent and Trademark Office.

PROFESSIONAL ACTIVITIES AND MEMBERSHIPS:

Bar Admissions – Virginia, 1997; Texas, 1988; District of Columbia, 1991

Registered to practice before the U.S. Patent and Trademark Office

Member – American Bar Association

Member – American Intellectual Property Law Association

Member – Federal Circuit Bar Association

Member and Past President – Giles S. Rich American Inn of Court

Member – Institute of Electrical and Electronics Engineers

Registered Professional Engineer (Texas)

Recognized by Best Lawyers in America

SELECTED PUBLICATIONS AND PRESENTATIONS:

Patents in Higher Education: Issues Arising From The Blackboard Case (presented May 2008)

Protect Your Patent Portfolio (April 2008)

The Patent Reform Act of 2007 (presented February 2008)

Opinions of Counsel: Have You Waived Attorney Work Product Without Knowing it? (presented January 2007)

Keeping A Good Thought Unpatentable, Intellectual Property Today, vol. 13, no. 7 (July 2006)

Another New Look at Prosecution History Estoppel: The Next Chapter of Festo, Virginia Intellectual Property Law Newslite, vol. 5, issue 3 (March 2004)

What's Left of Prosecution History Estoppel: The Next Chapter of Festo, Corporate Counsel, pp. A16-A17 (January 2004)

Inevitable Conduct - What Standard for Materiality?, posted on Mondaq.com (September 2003)

Festo - What the Federal Circuit and Supreme Court Have Done (presented June 2002)

Significant Patent Decisions from the Court of Appeals for the Federal Circuit - 1988, 38 Am. U.L. Rev. (1989)

A Tool for Aggregate Production Planning, Semiconductor International (May 1986)

REPRESENTATIVE CASES:

ATD Corp. v. Lydall, Inc., 159 F.3d 534 (Fed. Cir. 1998)

Advance Stores Co. v. Refinishing Specialties, Inc., 188 F.3d 408 (6th Cir. 1999)

Monsanto Co. v. Bayer Bioscience N.V., 363 F.3d 1235 (Fed. Cir. 2004)

EDUCATION:

B.S.E.E., Cornell University, 1978

M.B.A., University of Texas at Austin, 1980

J.D., with honors, University of Texas at Austin, 1988